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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : Sean C. Erickson
Serial No. : 10/627,289 ✓
Examiner : Hoa B. Trinh
Art Unit : 2814
Filed : July 25, 2003
For : LOW GATE RESISTANCE LAYOUT PROCEDURE
FOR RF TRANSISTOR DEVICES

Entered by
RCE filed
4/1/05.
S. Gault

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Commissioner For Patents
P.O. Box 1450
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RESPONSE TO THE FINAL OFFICE ACTION DATED DECEMBER 28, 2004

CERTIFICATE OF MAILING/TRANSMISSION (37 C.F.R. § 1.8)

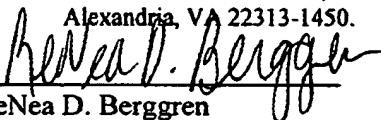
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ReNea D. Berggren

DATED: February 23, 2005

Dear Sir:

This is in response to the Office Action dated December 28, 2004, for which the three-month shortened statutory period is set to expire on March 28, 2004. While Applicant believes that no extension of time for response is necessary, should the Office determine that any additional fees are necessary for this response, the Commissioner is hereby authorized to charge any deficiencies to Deposit Account No. 12-2252.